

*as
co*
59. The memory system of claim 58, wherein the memory controller has a separate select output connected to the select signal input of each of the memory devices so that the memory controller can select one of the memory devices by causing the select signal output connected to the memory device to be selected to become active.

*a
b*
61. The memory system of claim 58, wherein the memory controller is configured to provide a plurality of separate select signal outputs and wherein the separate memory devices are arranged in a plurality of banks, with each bank comprising a plurality of memory devices, with a first one of the memory devices of each of the banks having a separate select signal input connected to a separate one of the select signal outputs of the memory controller.

REMARKS

The Examiner is invited to contact the below-signed attorney to discuss any questions which may remain with respect to the present application.

Respectfully submitted,

ROBERT D. NORMAN

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938
Minneapolis, MN 55402
(612) 373-6904

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By

Daniel J. Kluth
Daniel J. Kluth

Reg. No. 32,146

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